

FIG. 1 is a block diagram of a system 100. The system 100 includes a processor 101, a display device 105, an alpha-numeric input 106, an on screen cursor control 107, signal input output communication devices 108, a data storage device (optional) 104, a RAM volatile 102, and a ROM non-volatile 103. The processor 101 is connected to the display device 105, the alpha-numeric input 106, the on screen cursor control 107, the signal input output communication devices 108, the data storage device (optional) 104, the RAM volatile 102, and the ROM non-volatile 103. The display device 105 is connected to the alpha-numeric input 106. The alpha-numeric input 106 is connected to the on screen cursor control 107. The on screen cursor control 107 is connected to the signal input output communication devices 108. The signal input output communication devices 108 are connected to the data storage device (optional) 104. The data storage device (optional) 104 is connected to the RAM volatile 102. The RAM volatile 102 is connected to the ROM non-volatile 103.

100

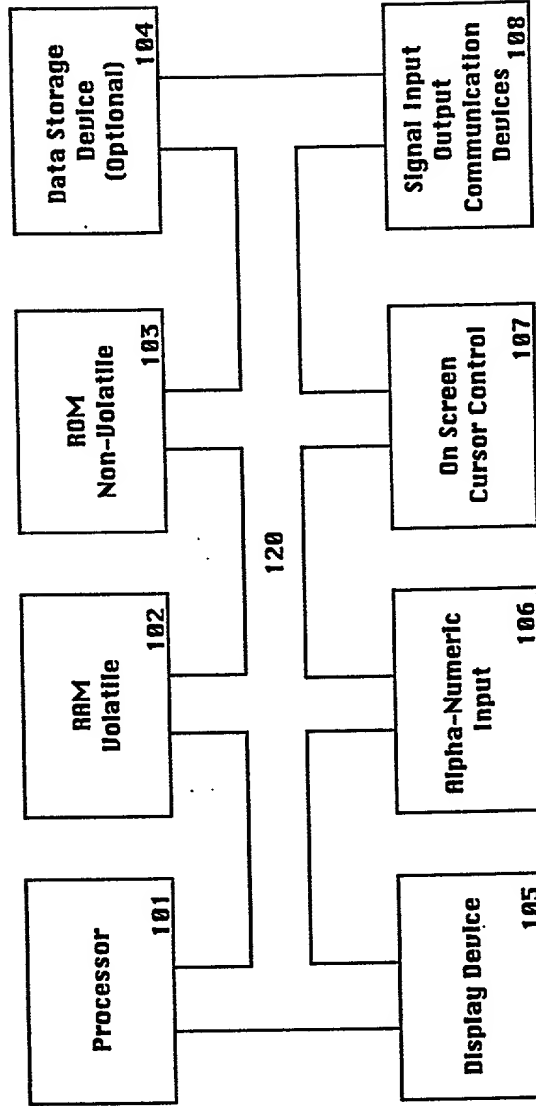


Fig. 1

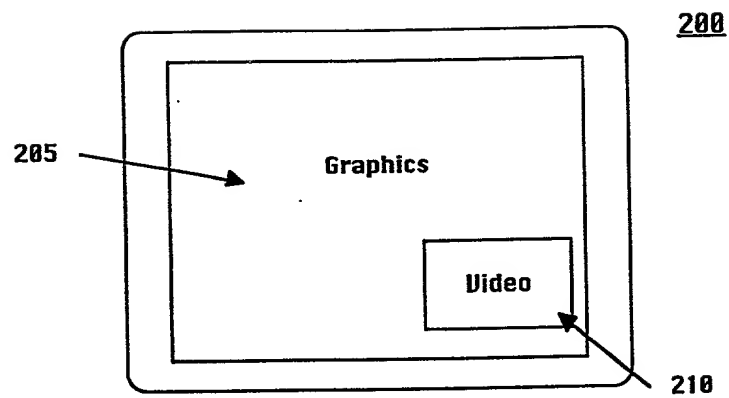


Fig. 2a

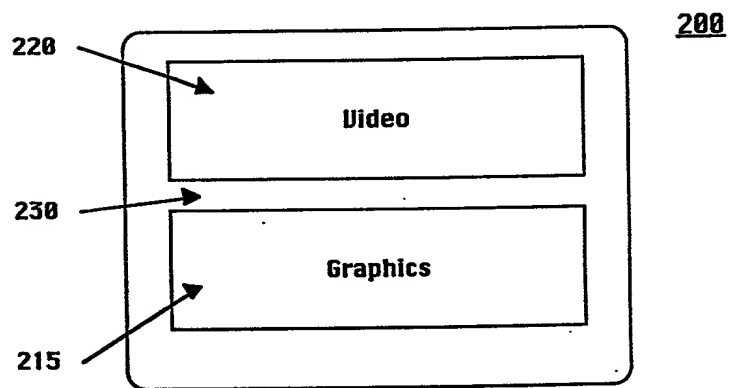


Fig. 2b

200

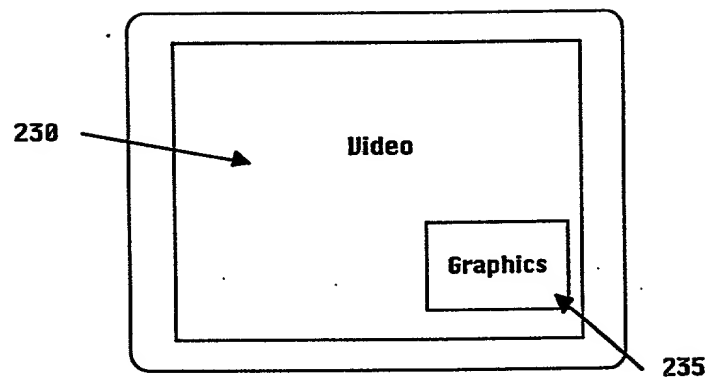


Fig. 2c

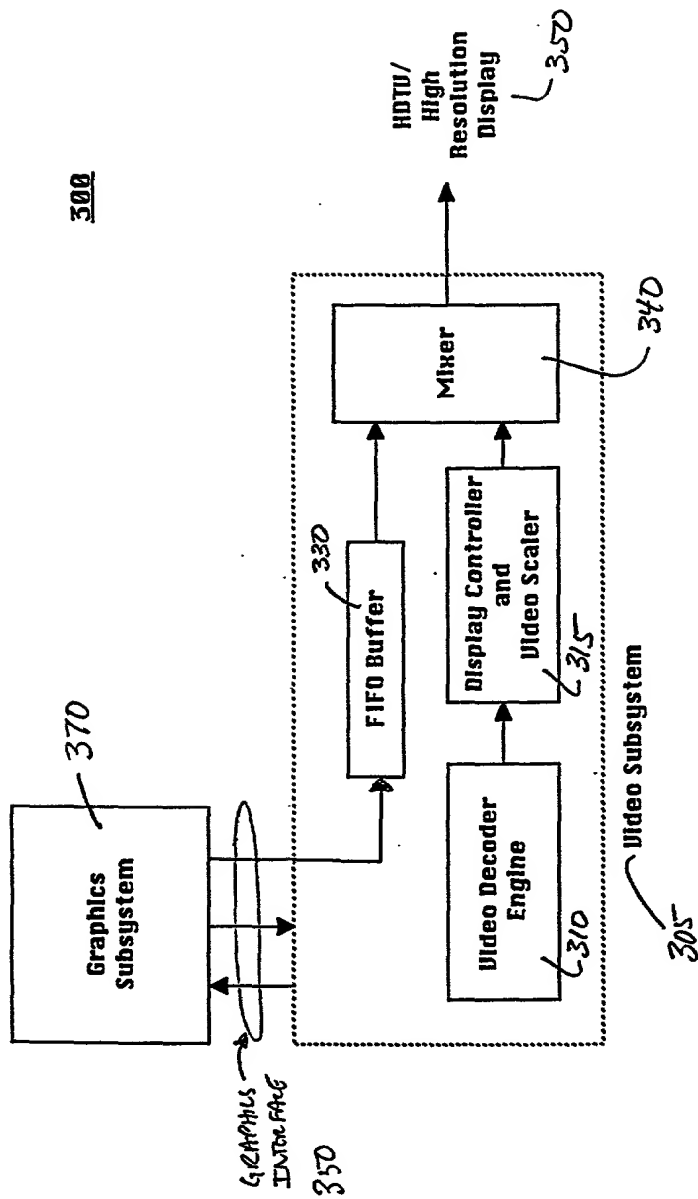


Fig. 3

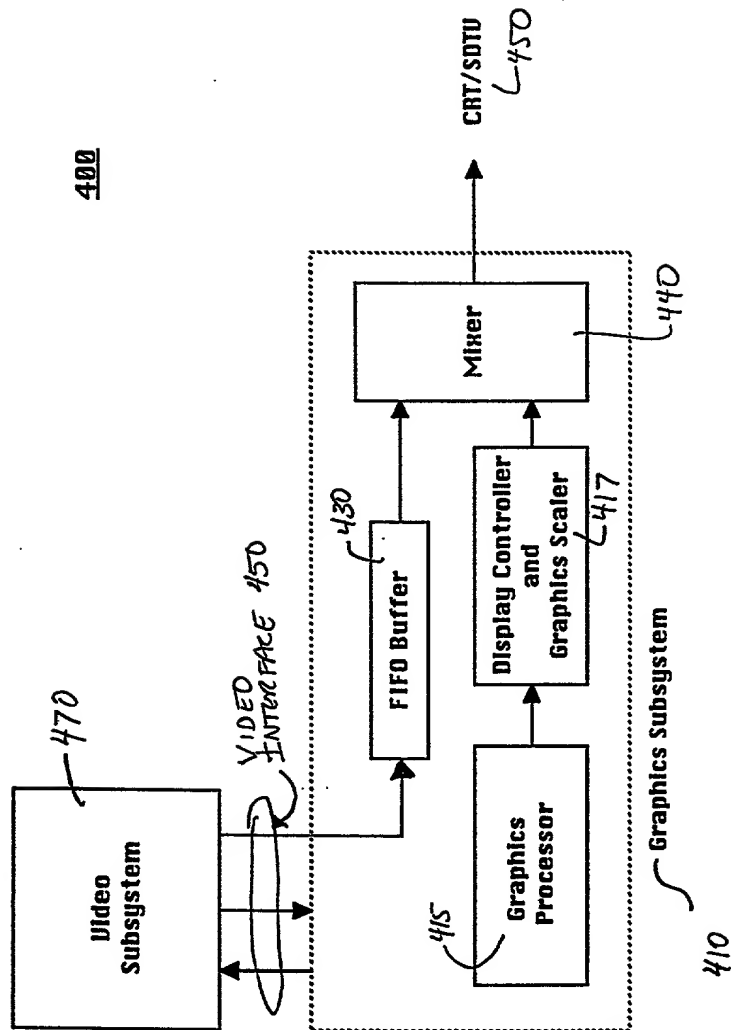


Fig. 4

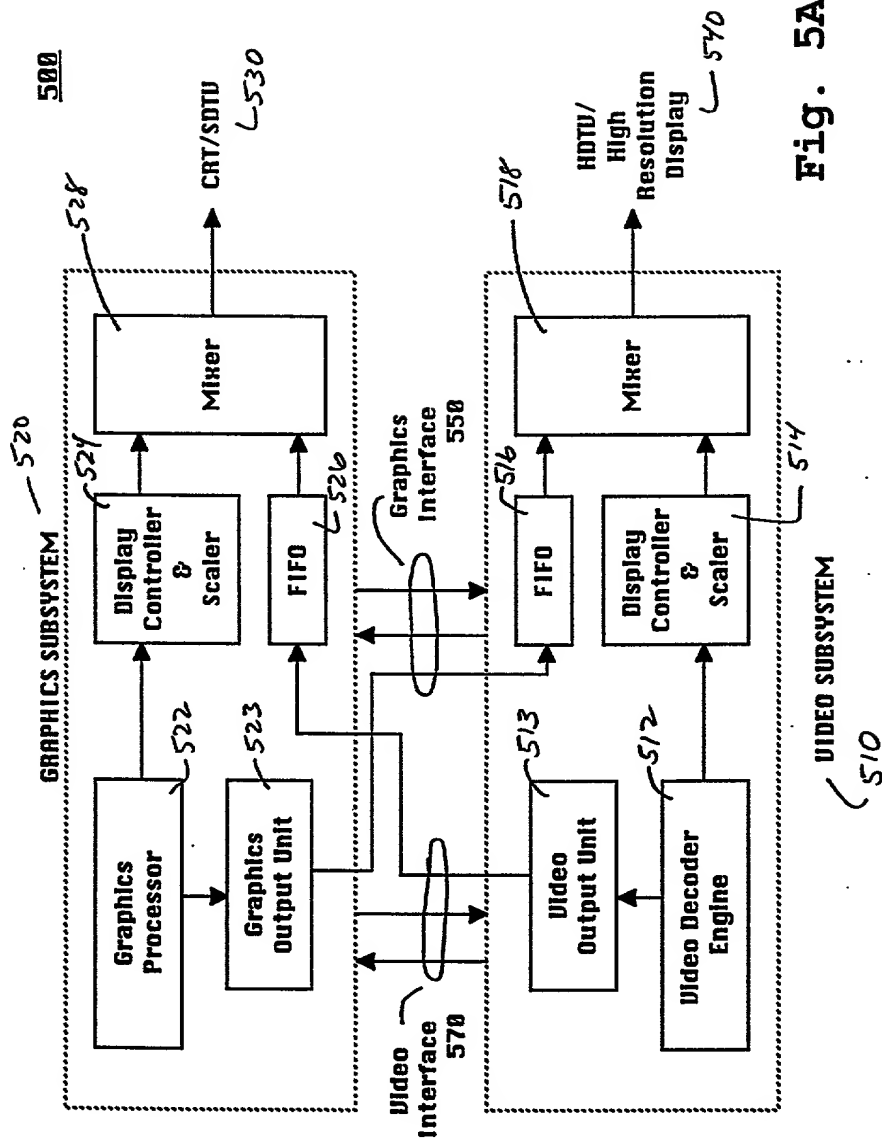


Fig. 5A

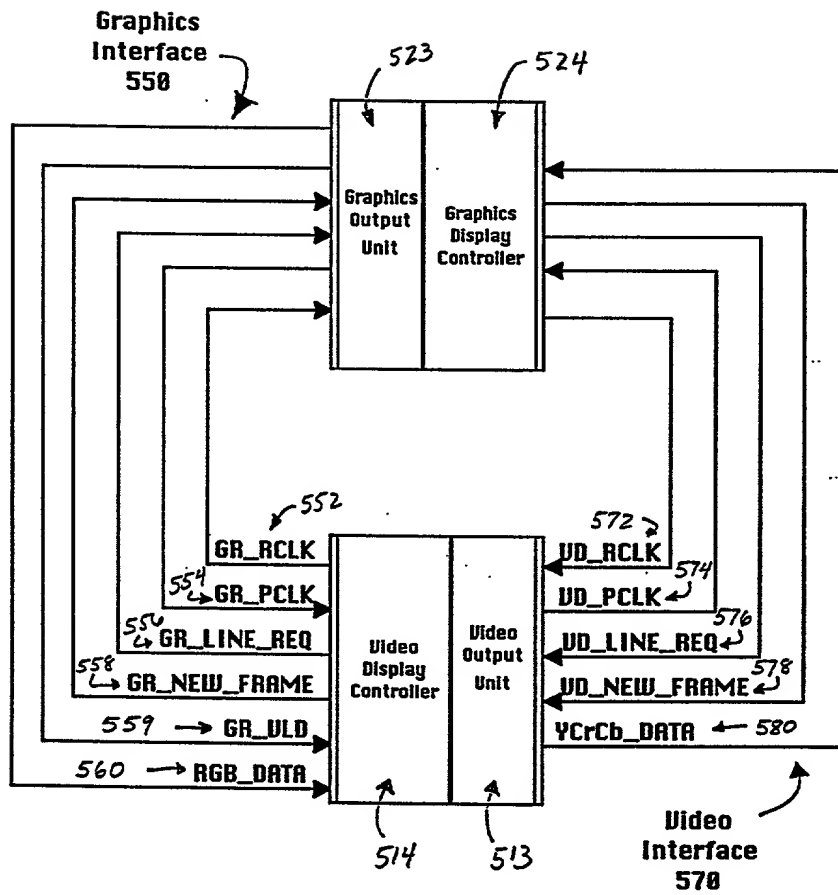


Fig. 5B

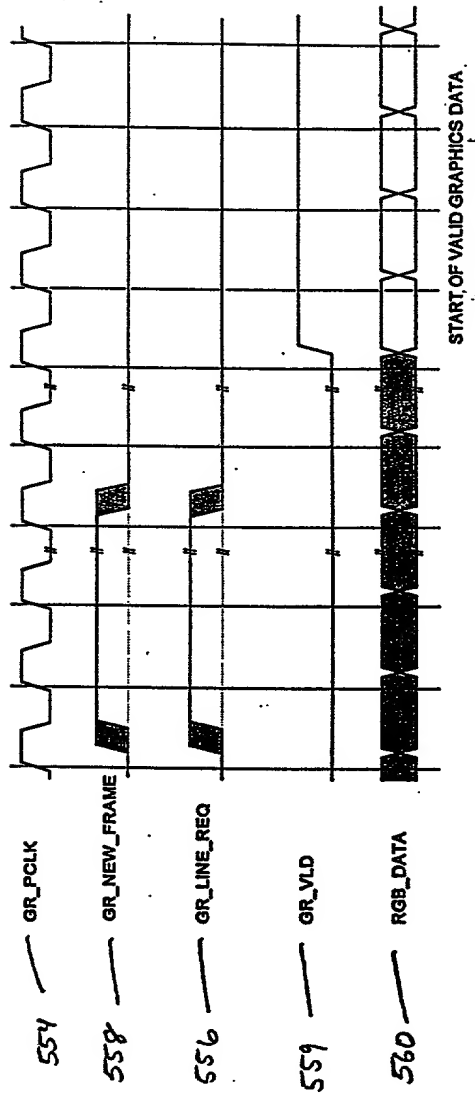


FIG. 6

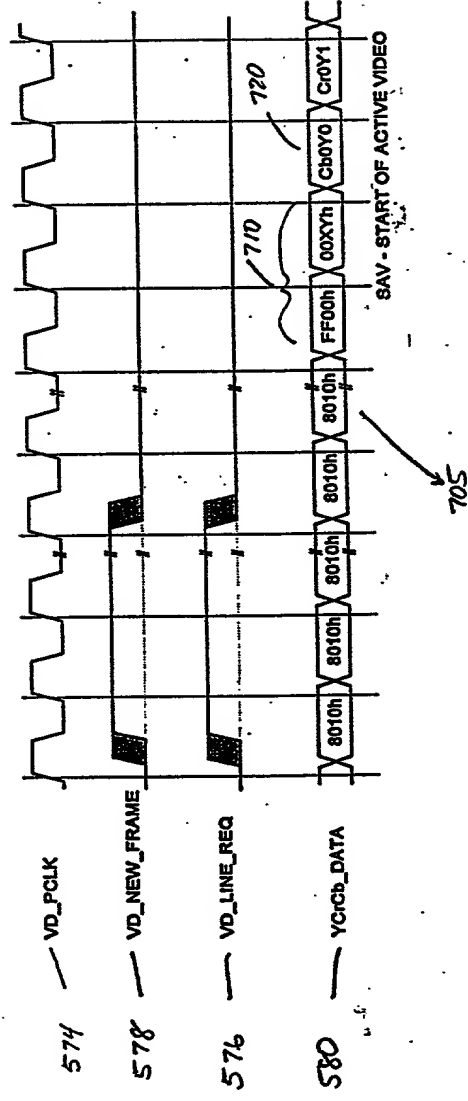


FIG. 7

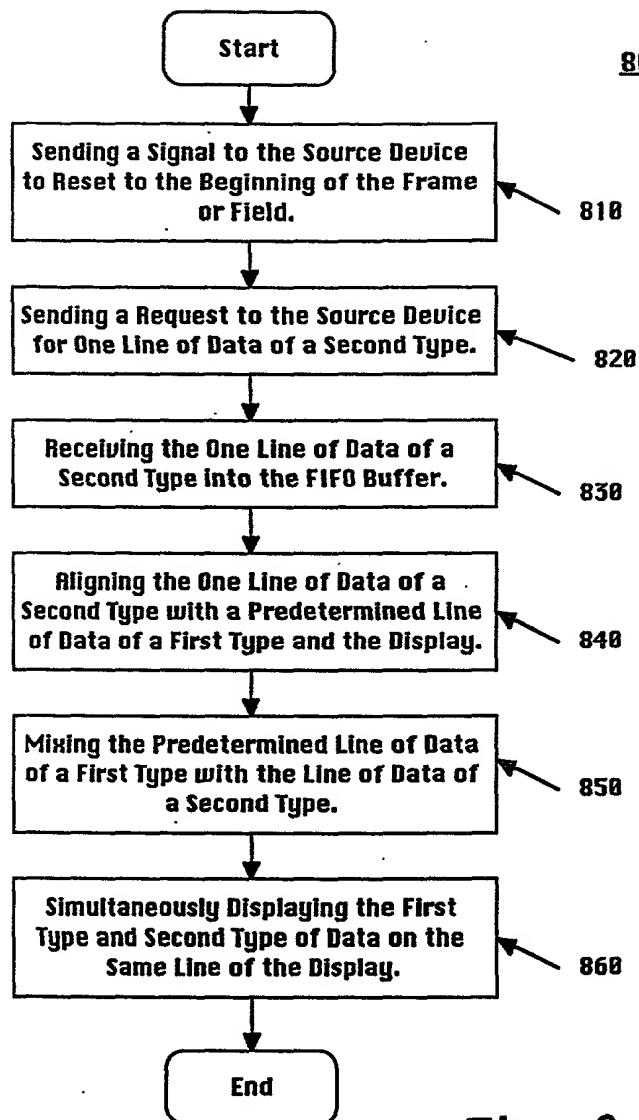


Fig. 8